

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	11800	((716/1-21) or (703/13-22)).CCLS.	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/11 10:09
S2	819	(703/13).CCLS.	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/01/11 10:10
S3	6988	(test\$4 or verif\$6 or validat\$4) with (soc or pga or fpga or pla or (programmable with logic))	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/11 10:59
S4	494	S1 and S3	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/01/11 10:11

S5	9553	(first and second) with (soc or pga or fpga or pla or (programmable with logic))	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TD B	OR	ON	2006/01/11 10:12
S6	146	S4 and S5	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TD B	OR	ON	2006/01/11 10:12
S7	97704	(waveform with generat\$4) or awg	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TD B	OR	ON	2006/01/11 10:14
S8	4	S7 and (tim\$4 with signal) and S6	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TD B	OR	ON	2006/01/11 10:46

S9	55988	S7 and (tim\$4 with signal)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TD B	OR	ON	2006/01/11 10:47
S10	7226	S9 and adder and differen\$6	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TD B	OR	ON	2006/01/11 10:47
S11	64	S10 and S5	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TD B	OR	ON	2006/01/11 10:48
S12	11	S11 and S3	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TD B	OR	ON	2006/01/11 10:53

S13	103	S3 and (disk with controller) and (awg or (signal with generat\$4))	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TD B	OR	ON	2006/01/11 10:55
S14	14	S13 and (adder or (add\$3 with circuit))	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TD B	OR	ON	2006/01/11 10:56
S15	420	((test\$4 or verif\$6 or validat\$4) with (soc or pga or fpga or pla or (programmable with logic))).ti.	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TD B	OR	ON	2006/01/11 11:00
S16	13	S15 and (awg or (signal with generat\$4)) and (adder or (add\$3 with circuit))	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TD B	OR	ON	2006/01/11 11:01

S17	9553	(first and second) with (soc or pga or fpga or pla or (programmable with logic))	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TD B	OR	ON	2006/01/11 15:04
S18	97704	(waveform with generat\$4) or awg	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TD B	OR	ON	2006/01/11 15:04
S19	55988	S18 and (tim\$4 with signal)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TD B	OR	ON	2006/01/11 15:04
S20	7226	S19 and adder and differen\$6	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TD B	OR	ON	2006/01/11 15:04

S21	64	S20 and S17	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TD B	OR	ON	2006/01/11 15:04
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Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L6	68	(703/19).CCLS.	US-PGPU B	OR	OFF	2006/01/12 15:45
L7	65	(703/21).CCLS.	US-PGPU B	OR	OFF	2006/01/12 15:44
L8	182	(703/13).CCLS.	US-PGPU B	OR	OFF	2006/01/12 15:44
L9	85	(716/16).CCLS.	US-PGPU B	OR	OFF	2006/01/12 15:45
L11	704	(716/4).CCLS.	US-PGPU B	OR	OFF	2006/01/12 15:48